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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Application Number	Not Yet Assigned 10/699652
				Filing Date	Concurrently Herewith
				First Named Inventor	Hagop A. Nazarian
				Art Unit	2827
				Examiner Name	Not Yet Assigned T.T. Nguyen
Sheet	1	of	1	Attorney Docket Number	M4065.0901/P901

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
TNT		6,356,477	3/2002	Tran	365 171
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FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
TNT		M. Durlam, et al., "A Low Power 1Mbit MRAM based on 1T1MTJ Bit Cell Integrated With Copper Interconnects", 2002 Symposium on VLSI Circuits Digest of Technical Papers; March 2, 2002.			
		R. Desikan, et al., "On-Chip MRAM as a High-Bandwidth, Low-Latency Replacement for DRAM Physical Memories", Department of Computer Sciences, Tech Report TR-02-47, University of Texas at Austin, September 27, 2002.			
		James Daughton, "Magnetoresistive Random Access Memory (MRAM)", February 24, 2000 (contact Arthur at daughton@nic.com).			
TNT		R. Butner, "Computing Unplugged", http://www.research.ibm.com/thinkresearch/pages/2001/20010202_mram.shtml ; visited 4/7/2003			

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04/13/05